

Version	Date	Author	Change Note	Approved
V1.0	20160325	hxs	First edictor	
V1.0	20160420	hxs	1、修改 供电用 SYR82K C； 2、删除 VDD_CPU B、VDD_CPU 连接电源回路 3、R401 修成 0 K； 4、修改 R40 10 放置主 控页 注意 PCB 布局靠 控制器 5、修改 VREF_DR1、VREF_DR0 从 VCC 取电；	

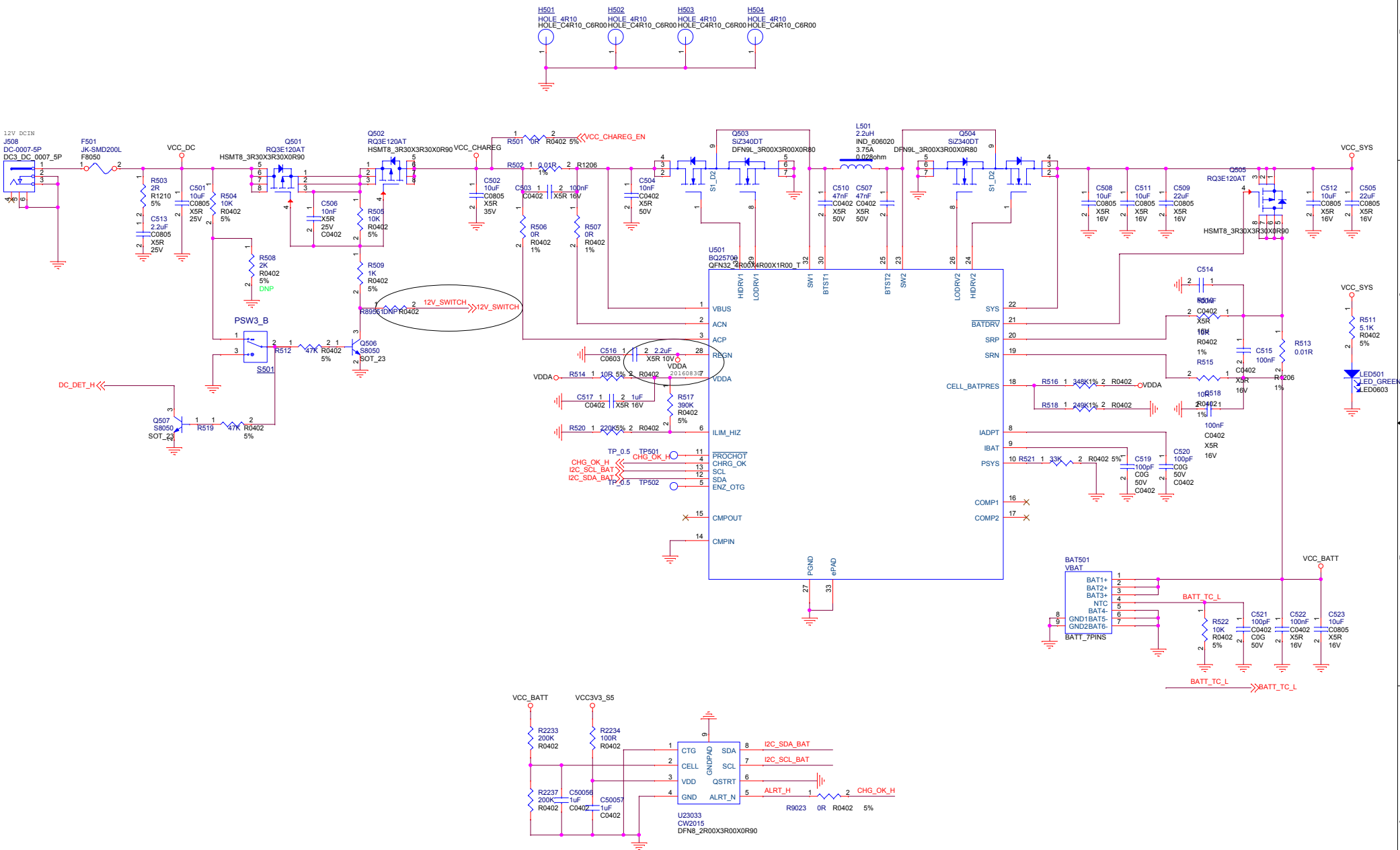
RK3399 I2C MAP

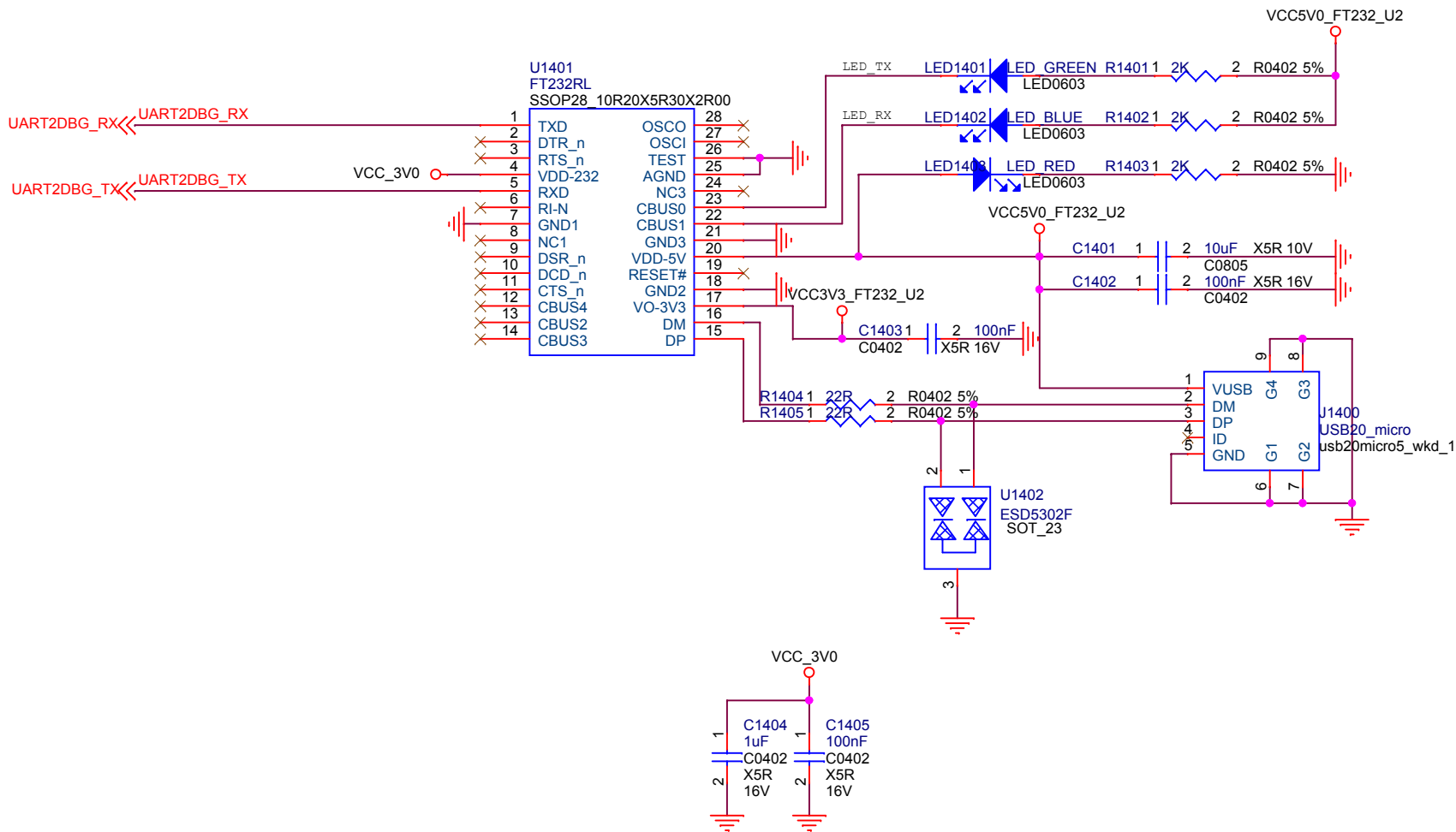
Port	Pin name	Domain	Bus name	Pull-up voltage	Slave Device	Slave Addr (MS 7Bits)	Note	Slave Bus Capability
I2C0	GPIO1_B7/SPI3_RXD/I2C0_SDA GPIO1_C0/SPI3_TXD/I2C0_SCL	PMUIO2 VCC1V8_PMU	I2C_SDA_PMIC I2C_SCL_PMIC	VCC1V8_PMU	TI LP8752-Q1	0x60	DC regulator	100kHz,400KHz,1MHz fast mode+ 3.4MHz high-speed mode
					Rockchip RK808	0x1b	PMIC	
					Fairchild FUSB302B	0x44,0x46	USB-TypeC Mux	100kHz,400KHz,1MHz fast mode+
I2C1	GPIO4_A1/I2C1_SDA GPIO4_A2/I2C1_SCL	APIO5 VCC1V8_IO	I2C_SDA_AUDIO I2C_SCL_AUDIO	VCCA1V8_CODEC	Everest ES8316	0x10	Audio codec	
			I2C_SDA_CAM I2C_SCL_CAM	VCCA1V8_CODEC	Omnivision OV8858	0x6c,0x20	MIPI Camera	
I2C2	GPIO2_A0/VOP_D0/CIF_D0/I2C2_SDA GPIO2_A1/VOP_D1/CIF_D1/I2C2_SCL	APIO2 VCC1V8_IO	Other pin function					
I2C3	GPIO4_C0/I2C3_SDA/UART2B_RX GPIO4_C1/I2C3_SCL/UART2B_TX	APIO4 VCC3V0_IO	I2C_SDA_HDMI I2C_SCL_HDMI	VCC3V0_IO				
I2C4	GPIO1_B3/I2C4_SDA GPIO1_B4/I2C4_SCL	PMUIO2 VCC1V8_IO	I2C_SDA_MEMS I2C_SCL_MEMS	VCC1V8_PMU	Capellamicro CM3218	0x10,0x0c	ALS Sensor	
					AsahiKasei AK8963C		Compass	
					InverSense MPU6500	0x34	Accer+Gyro	
			I2C_SDA_TOUCH I2C_SCL_TOUCH	VCC1V8_TOUCH	Silead GSL3680	0x40	Touch panel	
I2C5	GPIO3_B2/MAC_RXER/I2C5_SDA GPIO3_B3/MAC_CLK/I2C5_SCL	APIO1 VCC3V3_LAN	I2C_SDA_PCIE I2C_SCL_PCIE	VCC3V3_IO				
I2C6	GPIO2_B1/SPI2_RXD/CIF_HREF/I2C6_SDA GPIO2_B2/SPI2_TXD/CIF_CLKIN/I2C6_SCL	APIO2 VCC1V8_IO	I2C_SDA_BAT I2C_SCL_BAT	VCC1V8_DVP	Chrome Battery	0x19	Battery	100KHz
					TI BQ25700	0x6a (I2C) 0x09 (SMBus)	Charger	
					Fairchild FUSB302B	0x44,0x46	USB-TypeC Mux	100kHz,400KHz,1MHz fast mode+
I2C7	GPIO2_A7/VOP_D7/CIF_D7/I2C7_SDA GPIO2_B0/VOP_CLK/CIF_VSYNC/I2C7_SCL	APIO2 VCC1V8_IO	Other pin function					

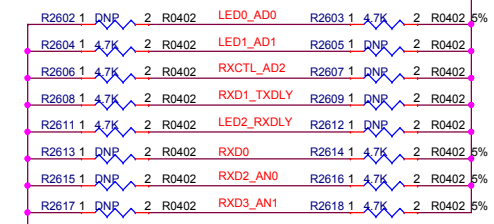
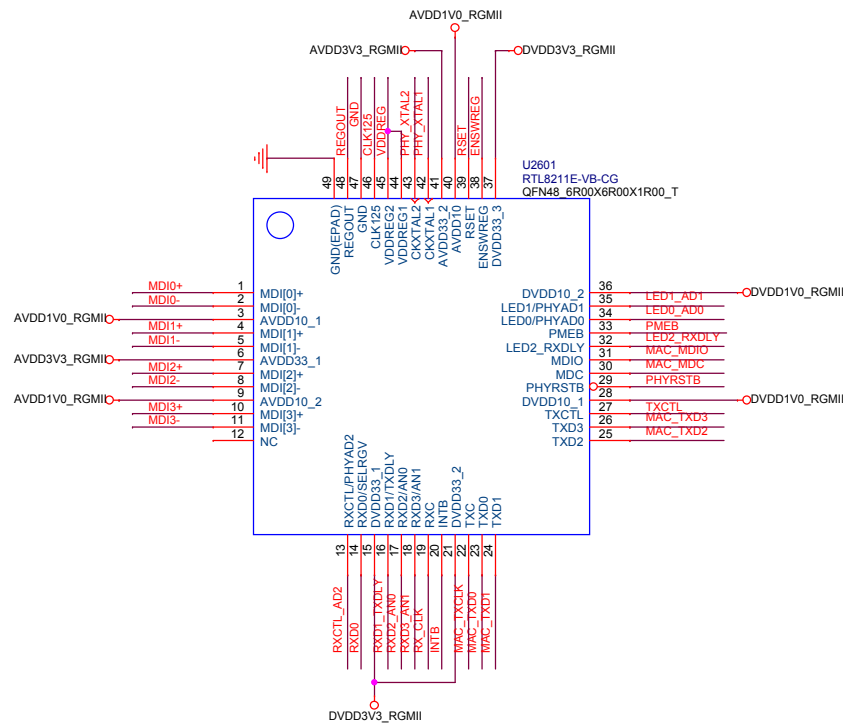
		Fuzhou Rockchip Electronics	
瑞芯微电子			
Project:	SCHEMATIC1		
File:	I2C MAP		
Date:	Wednesday, November 09, 2016	Rev:	V1.0
Designed by:	Linus	Sheet:	2 of 19

RK3399 Power Domain Map

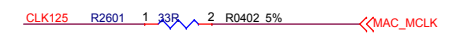
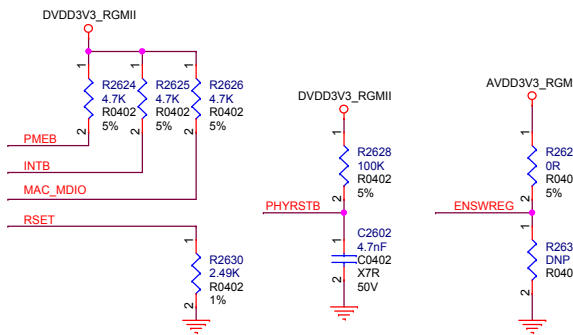
Part Port	Domain	Pin name in datasheet	I/O type	Power supply	Power source
Part C	PMUIO1	pmuiol_gpio0ab	1.8V only	VCC1V8_PMU	RK808 VLDO3
Part E	PMUIO2	pmu1830_gpio1abcd	1.8V (Default) 3.0V	VCC1V8_PMU	RK808 VLDO3
Part I	APIO1	gmac_gpio3abc	3.3V only	VCC1V8_IO VCC3V3_IO	RK808 Buck4 RK808 VSW2
Part L	APIO2	bt656_gpio2ab	1.8V (Default) 3.0V	VCC1V8_DVP	RK808 VLDO1
Part G	APIO3	wifi/bt_gpio2cd	1.8V only	VCC1V8_WIFI	RK808 Buck4
Part K	APIO4	gpio1830_gpio4cd	1.8V 3.0V (Default)	VCC_1V5 VCC3V0_IO	RK808 VLDO6 RK808 VLDO8
Part J	APIO5	audio_gpio3d_gpio4a	1.8V (Default) 3.0V	VCCA1V8_CODEC	RK808 VLDO7
Part F	SDMMC0	sdmmc_gpio4b	1.8V 3.0V (Default)	VCC_SDIO	RK808 VLDO4



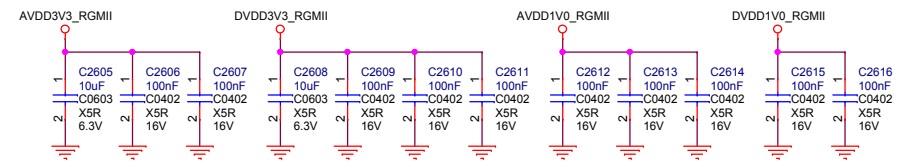
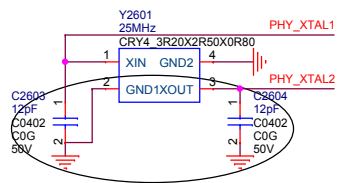
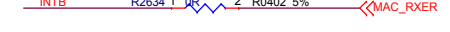
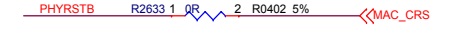
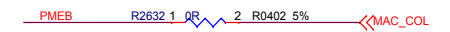
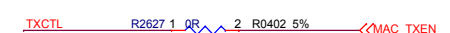
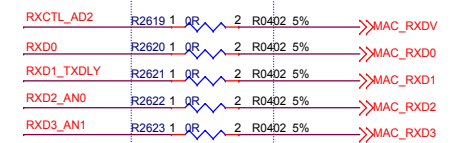
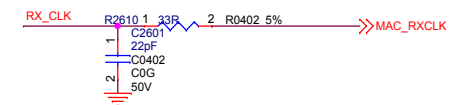




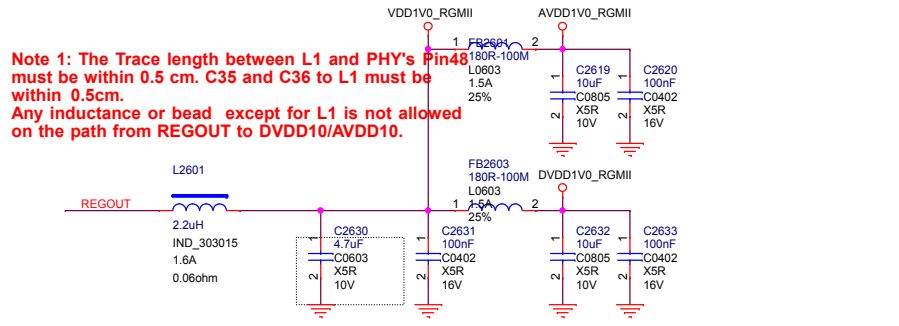
R61,R62: Config for all capability
R51,R54,R148: PHY Address=001(RTL8211E)
R64,R50: Without TX/RX Delay



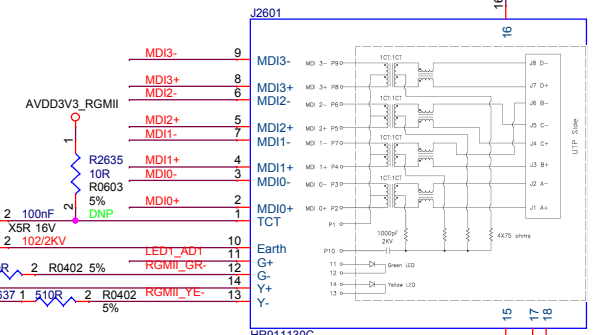
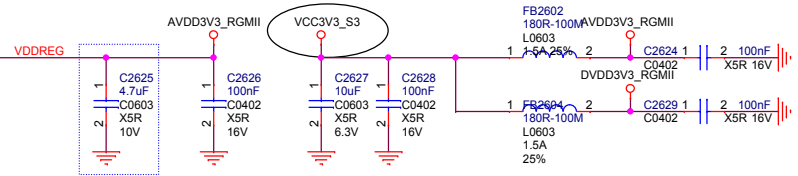
Place filter network close to RX_CLK.
Reserved for EMI



Note 1: The Trace length between L1 and PHY's Pin48 must be within 0.5 cm. C35 and C36 to L1 must be within 0.5cm. Any inductance or bead except for L1 is not allowed on the path from REGOUT to DVDD10/AVDD10.

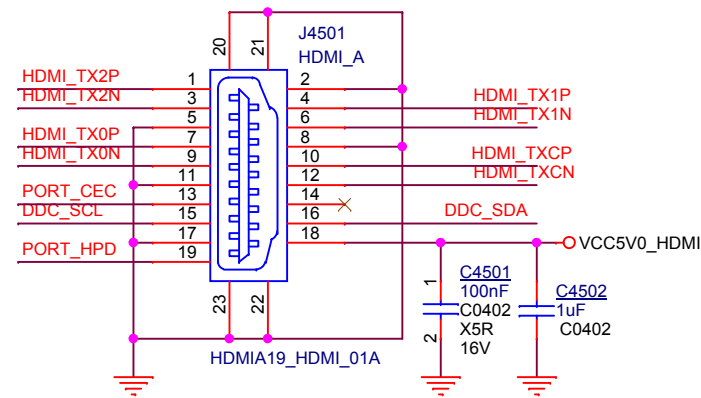
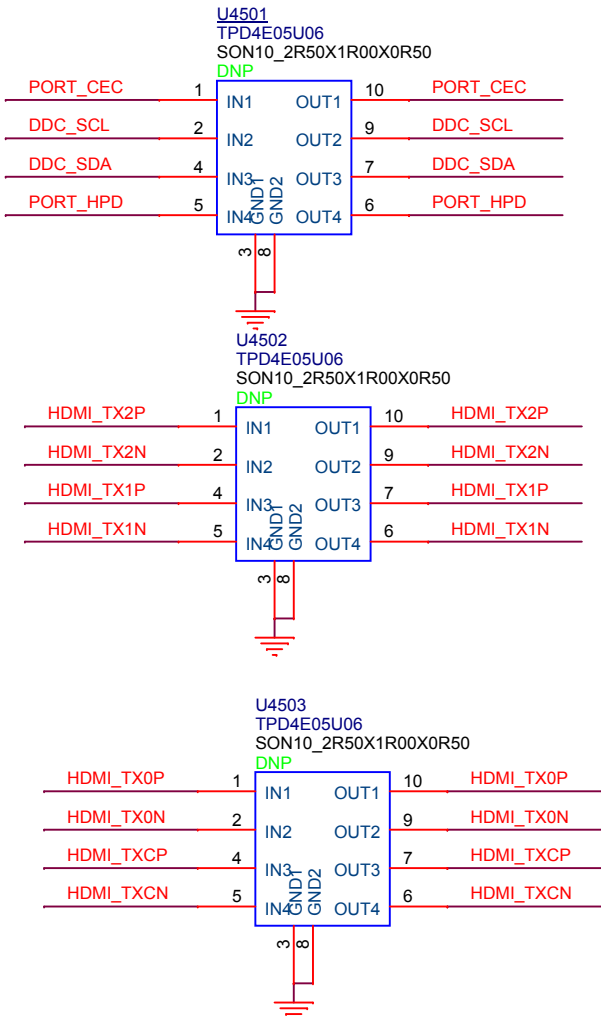
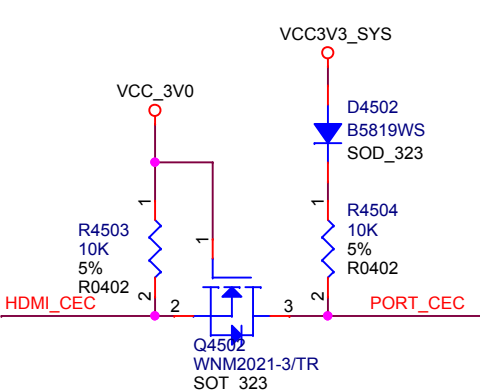
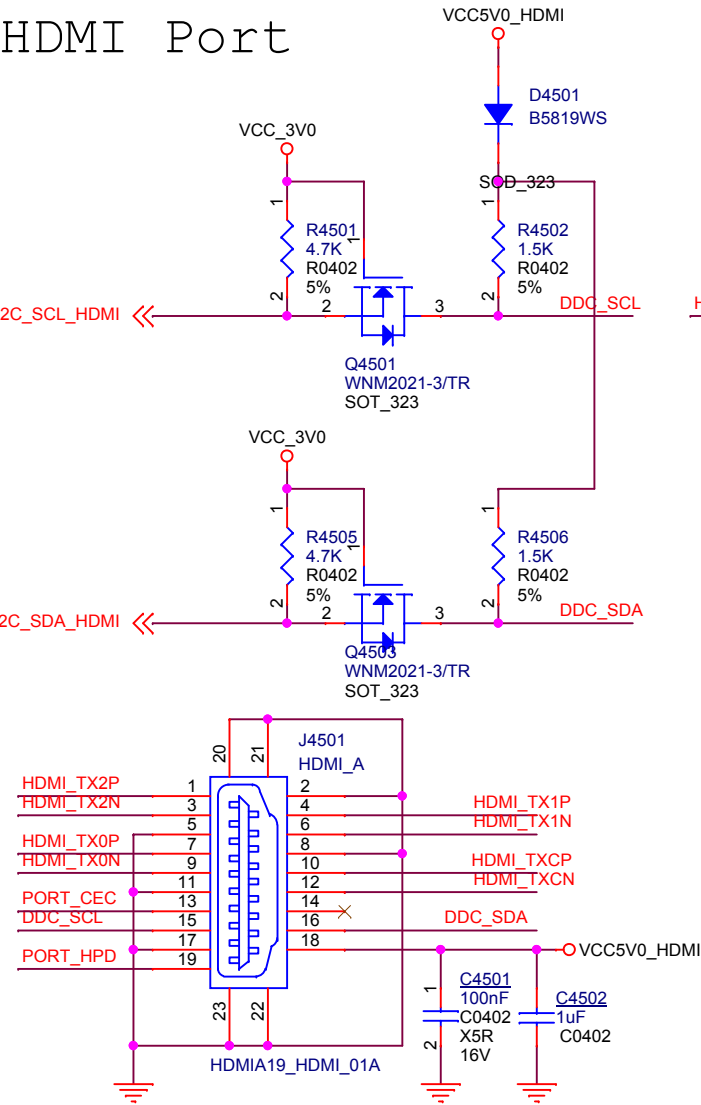


Note 2: The Trace length from C56, C57 to Pin 44,45(VDDREG) must be within 0.5 cm. The trace width from AVDD33 to Pin 44,45 should>40mils



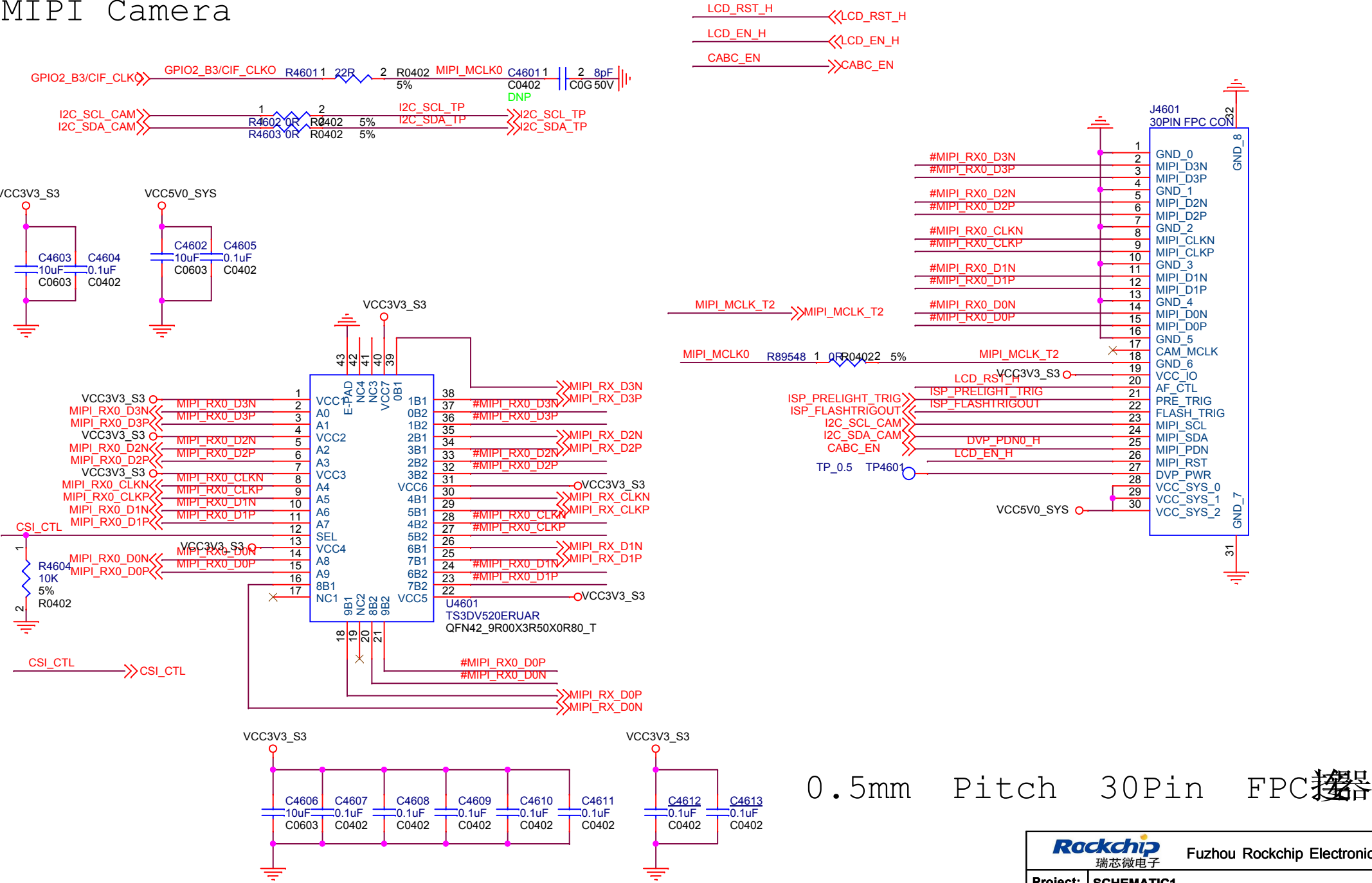
HR91130C
 RJ45_HR91130C

HDMI Port

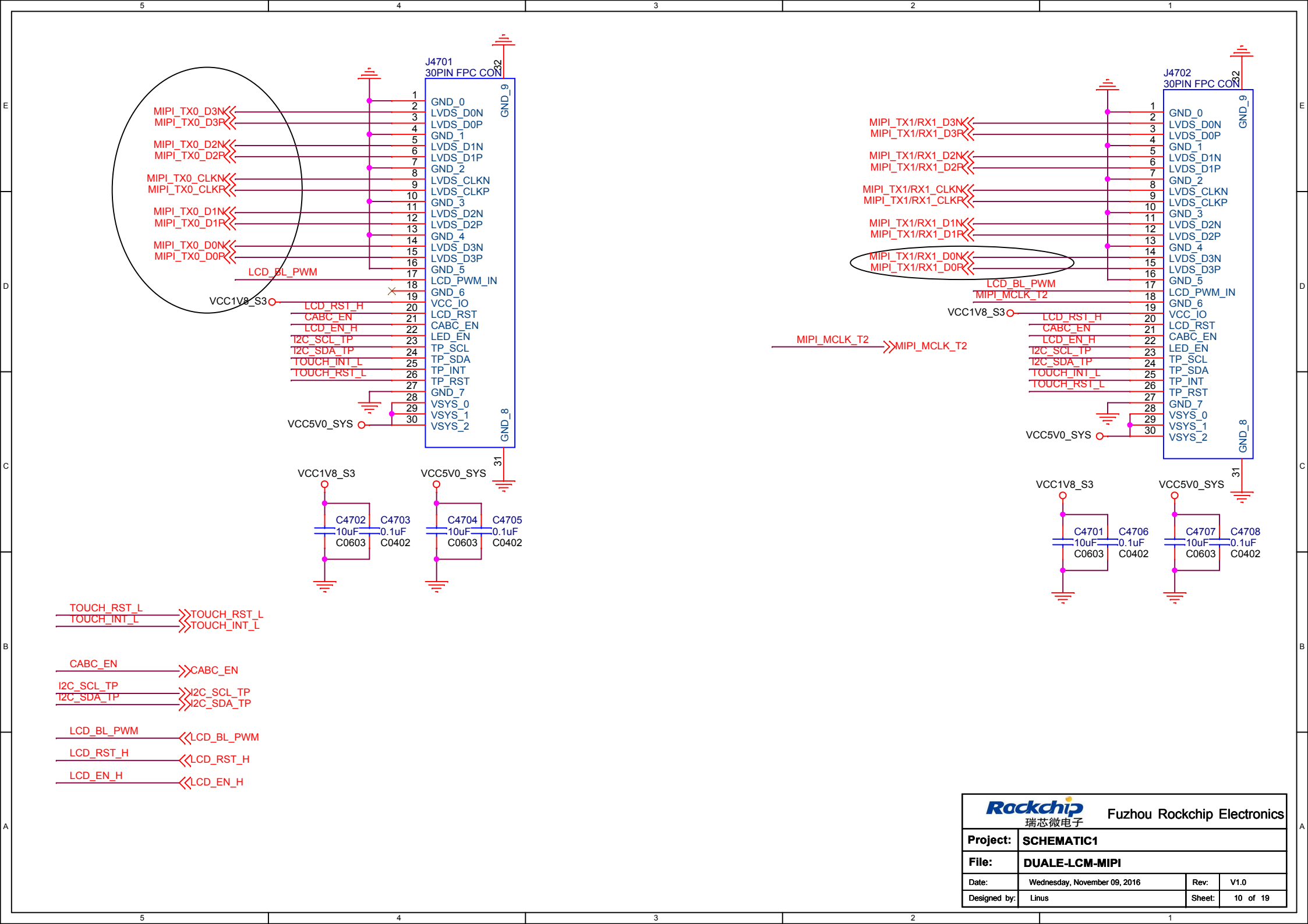


- HDMI_TX0P
- HDMI_TX0N
- HDMI_TX1P
- HDMI_TX1N
- HDMI_TX2P
- HDMI_TX2N
- HDMI_TXCP
- HDMI_TXCN
- HDMI_CEC
- PORT_HPDP
- I2C_SCL_HDMI
- I2C_SDA_HDMI

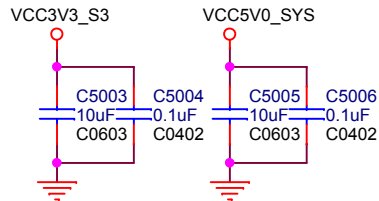
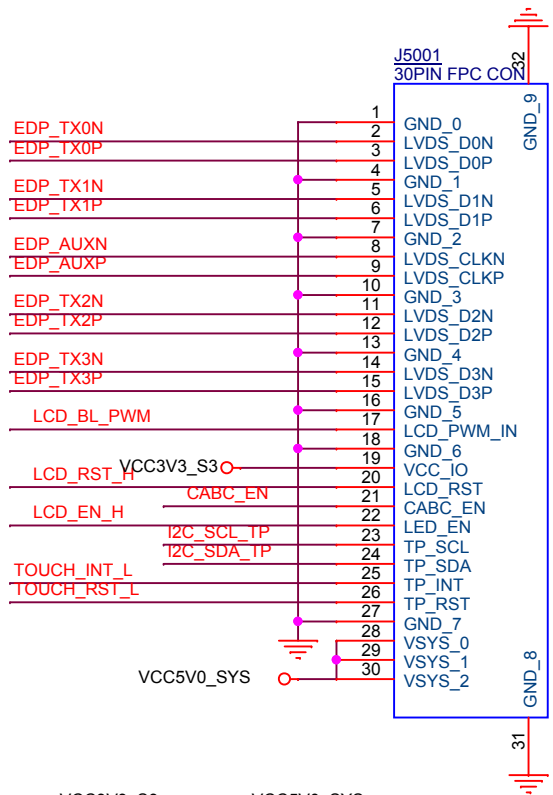
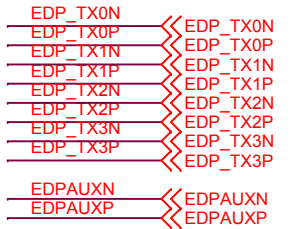
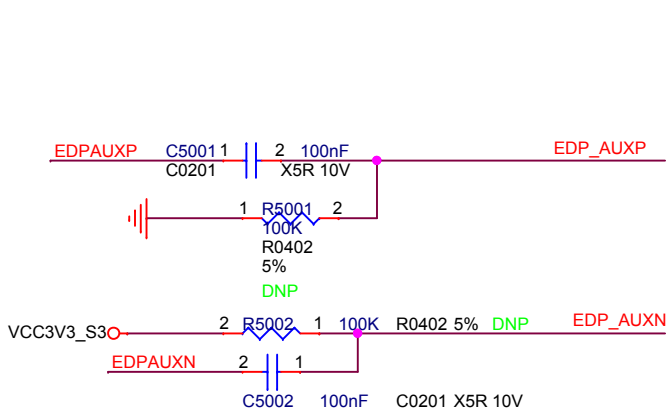
MIPI Camera



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Project:	SCHEMATIC1		
File:	MIPI CSI		
Date:	Wednesday, November 09, 2016	Rev:	V1.0
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eDP Panel



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Project:	SCHEMATIC1		
File:	eDP		
Date:	Wednesday, November 09, 2016	Rev:	V1.0
Designed by:	Linus	Sheet:	11 of 19

WIFI/WIFI ac/BT MODULE

50 Ohm RF trace

50 Ohm RF trace

U8101 AP6354 MD44 AP6XXX 13RX15RX

Y6101 37.4MHz CRY4_3R20X2R50X0R80

WIFI_REG_ON_H

WIFI_HOST_WAKE_L

SDIO0_CMD

SDIO0_D0

SDIO0_D1

SDIO0_D2

SDIO0_D3

SDIO0_CLK

UART0_RTS

UART0_TXD

UART0_RXD

UART0_CTS

BT_HOST_WAKE_L

BT_WAKE_L

BT_REG_ON_H

WIFI_REG_ON_H

WIFI_HOST_WAKE_L

SDIO0_CMD

SDIO0_D0

SDIO0_D1

SDIO0_D2

SDIO0_D3

SDIO0_CLK

UART0_RTS

UART0_TXD

UART0_RXD

UART0_CTS

BT_HOST_WAKE_L

BT_WAKE_L

BT_REG_ON_H

VDDIO_WL

VCC3V3_SYS

Note: VBAT 範圍 3.0V~4.8V , 電阻至少 400 歐

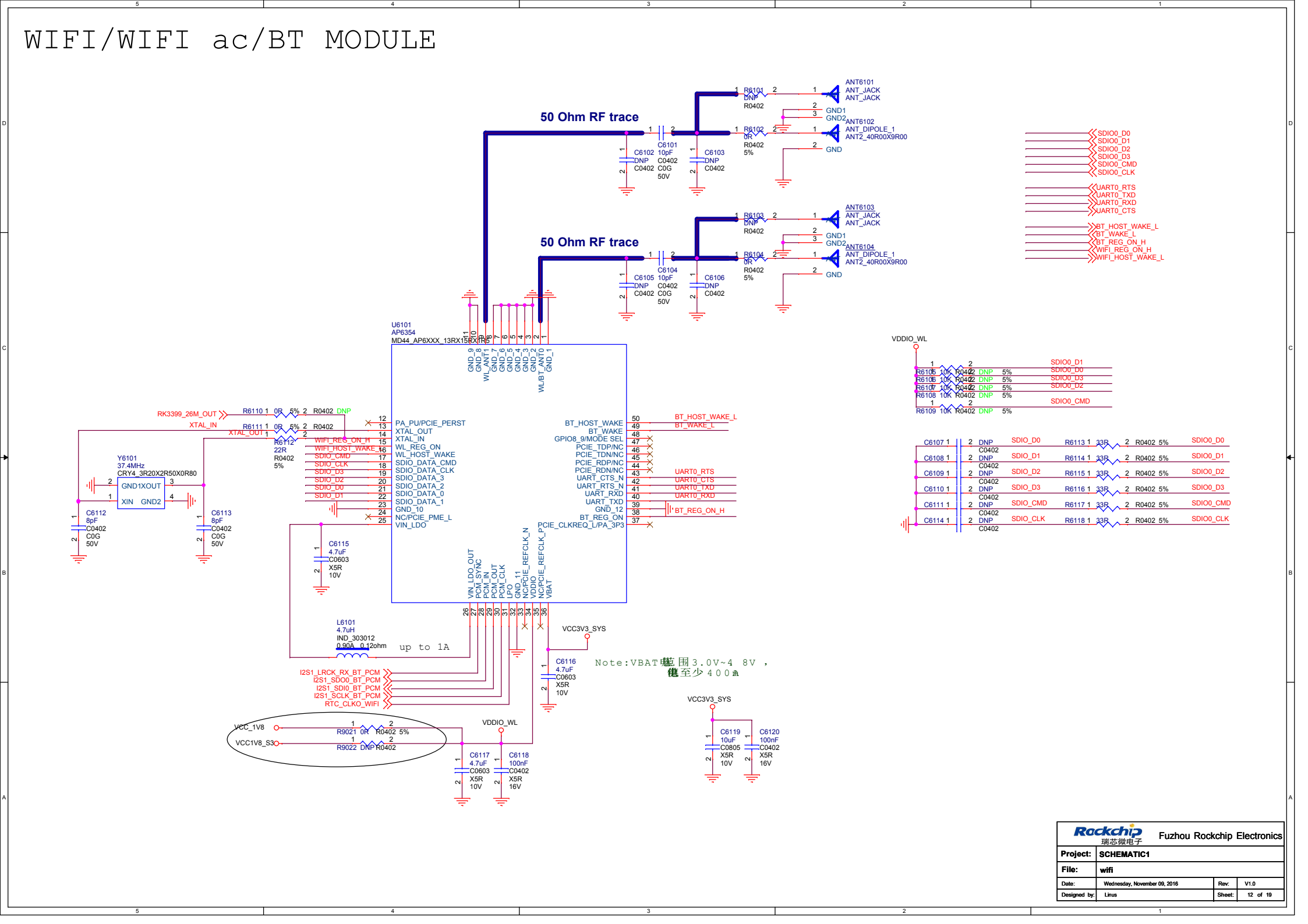
Rockchip 瑞芯微电子 Fuzhou Rockchip Electronics

Project: SCHEMATIC1

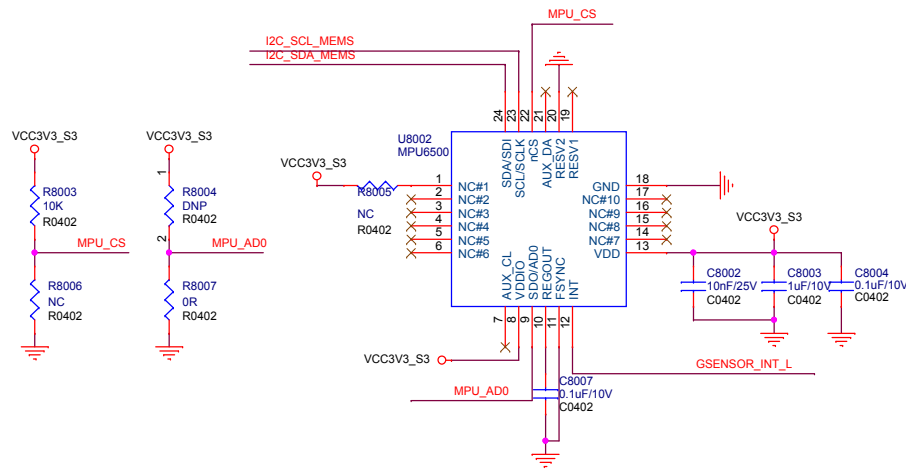
File: wifi

Date: Wednesday, November 09, 2016 Rev: V1.0

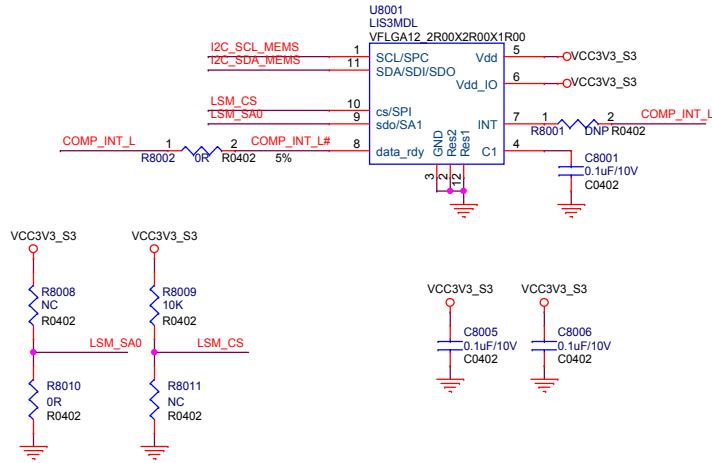
Designed by: Linus Sheet: 12 of 19



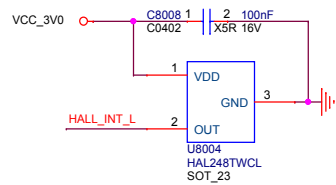
Gyroscope+G-sensor



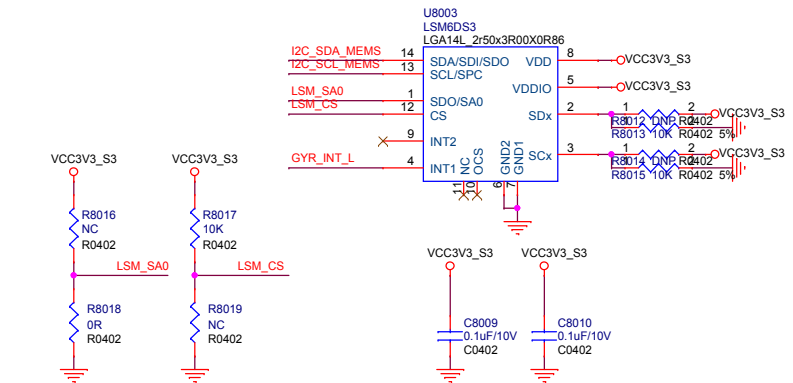
Compass



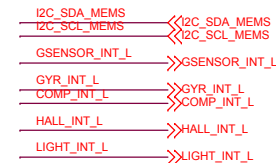
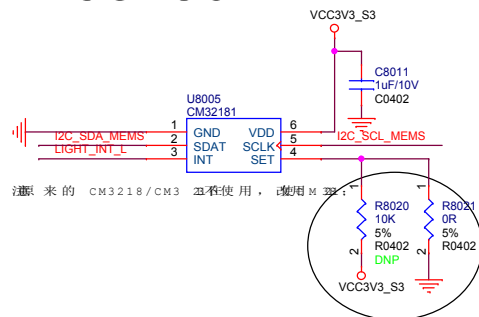
HALL IC



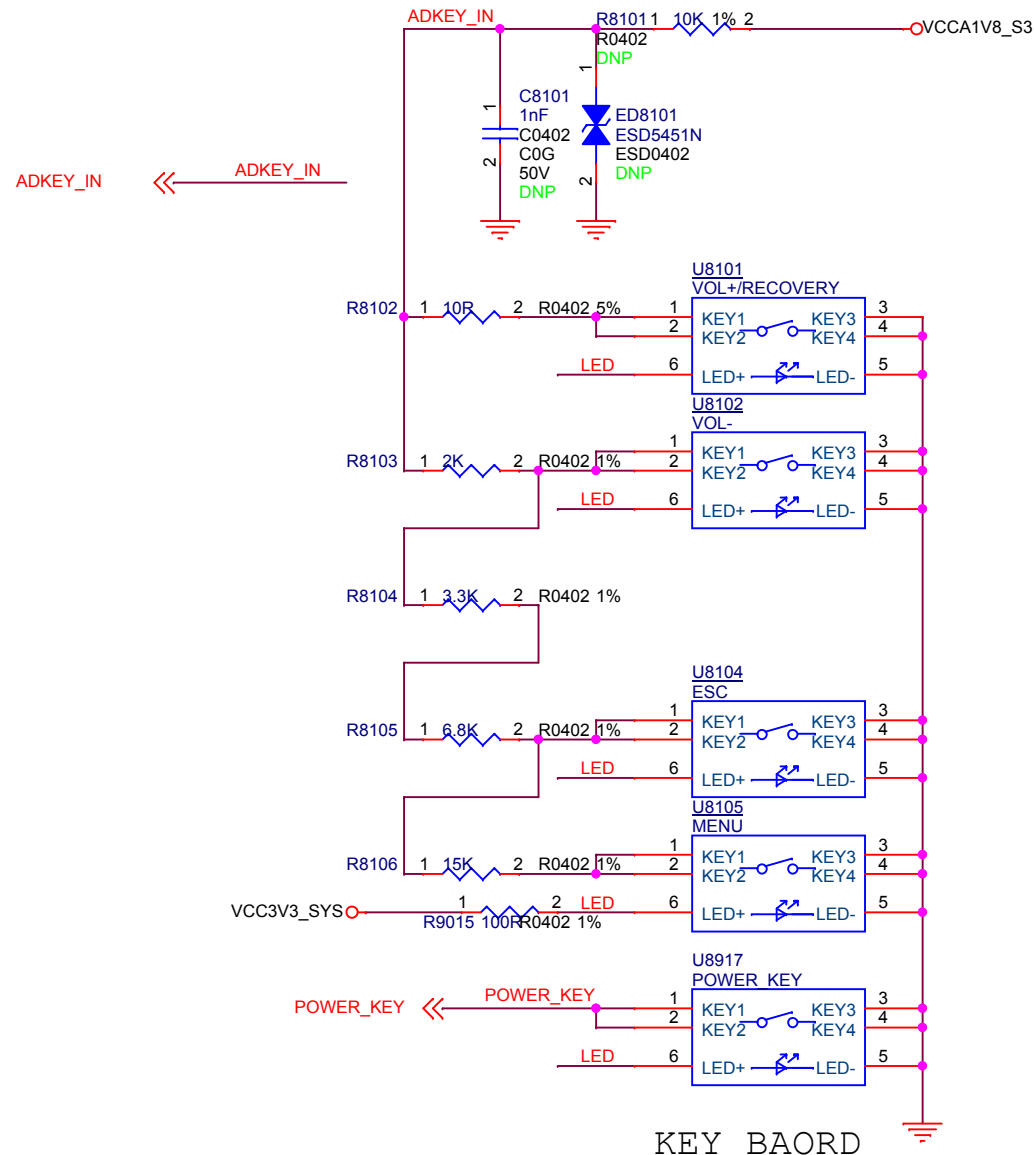
Gyroscope



LIGHT-Sensor



 Fuzhou Rockchip Electronics	
Project:	SCHEMATIC1
File:	sensor
Date:	Thursday, November 10, 2016
Designed by:	Linus
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KEY BAORD

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Project:	SCHEMATIC1		
File:	tf card		
Date:	Wednesday, November 09, 2016	Rev:	V1.0
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PCI.Express

